

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Serial No. | 10/707,287 | Conf. No. | 1286 |
| In Re Application of: | Gray et al. | Art Unit: | 2825 |
| Filed: | 12/03/2003 | Dkt. #: | BUR920030123US1 (IBMB-0036) |
| Title: | CIRCUIT AREA MINIMIZATION USING SCALING | Examiner: | Dimyan, Magid Y. |

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

I. INTRODUCTORY COMMENTS

In response to the Office Action of February 24, 2006, please reconsider the above-referenced patent application in view of the following amendments and remarks: